

FIG. 1

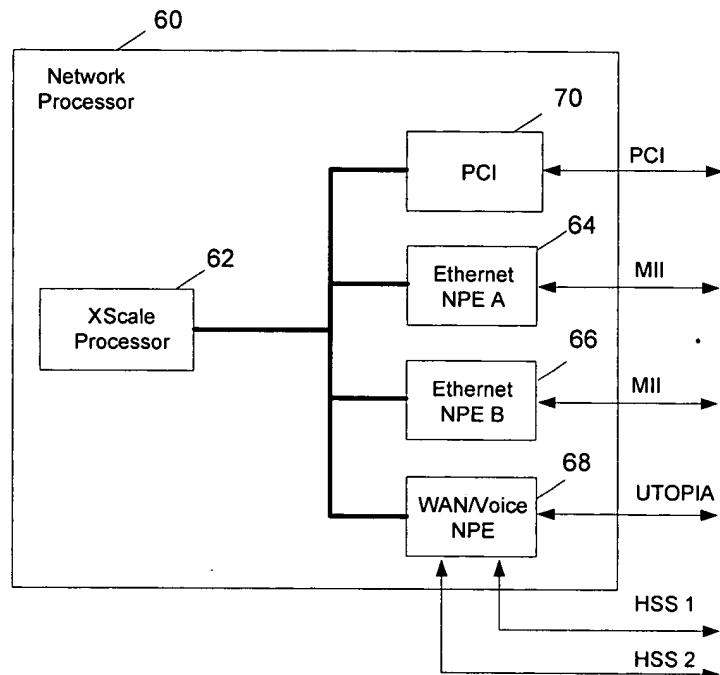


FIG. 2

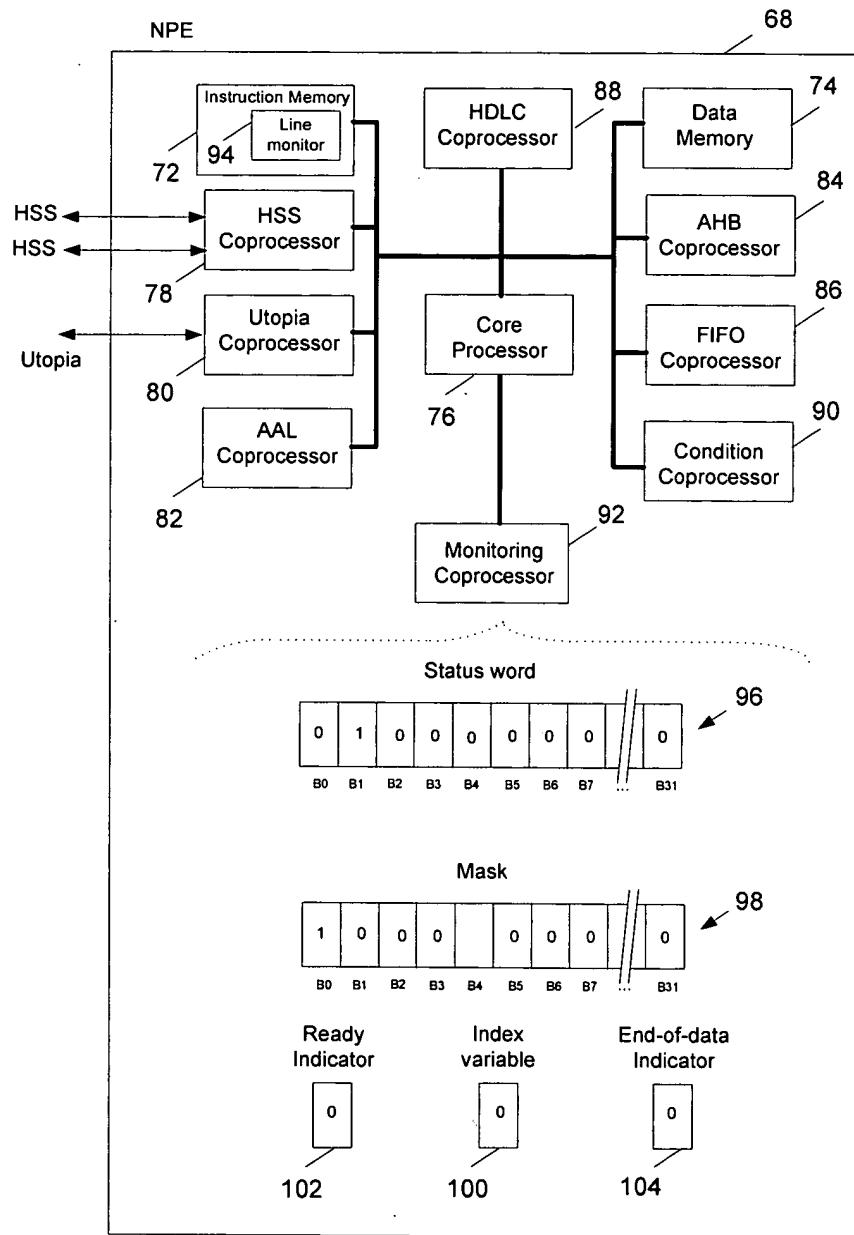
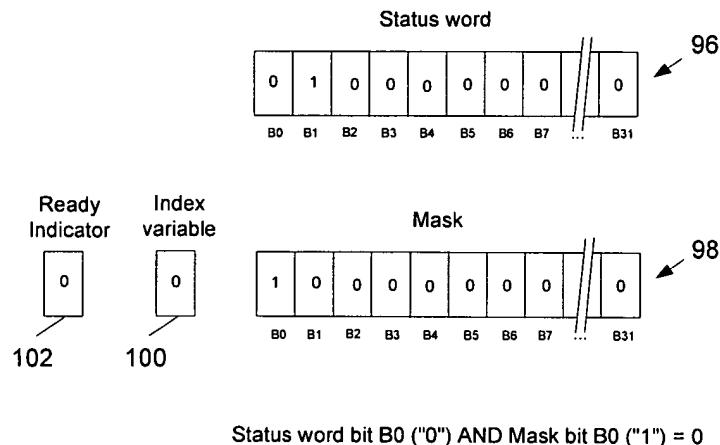
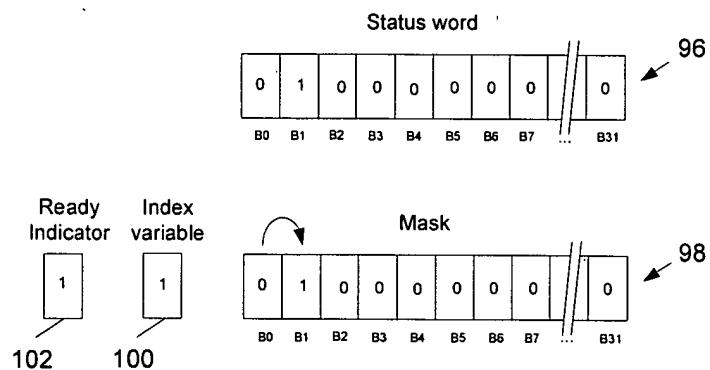


FIG. 3



Status word bit B0 ("0") AND Mask bit B0 ("1") = 0

FIG. 4A



Status word bit B1 ("1") AND Mask bit B1 ("1") = 1

FIG. 4B

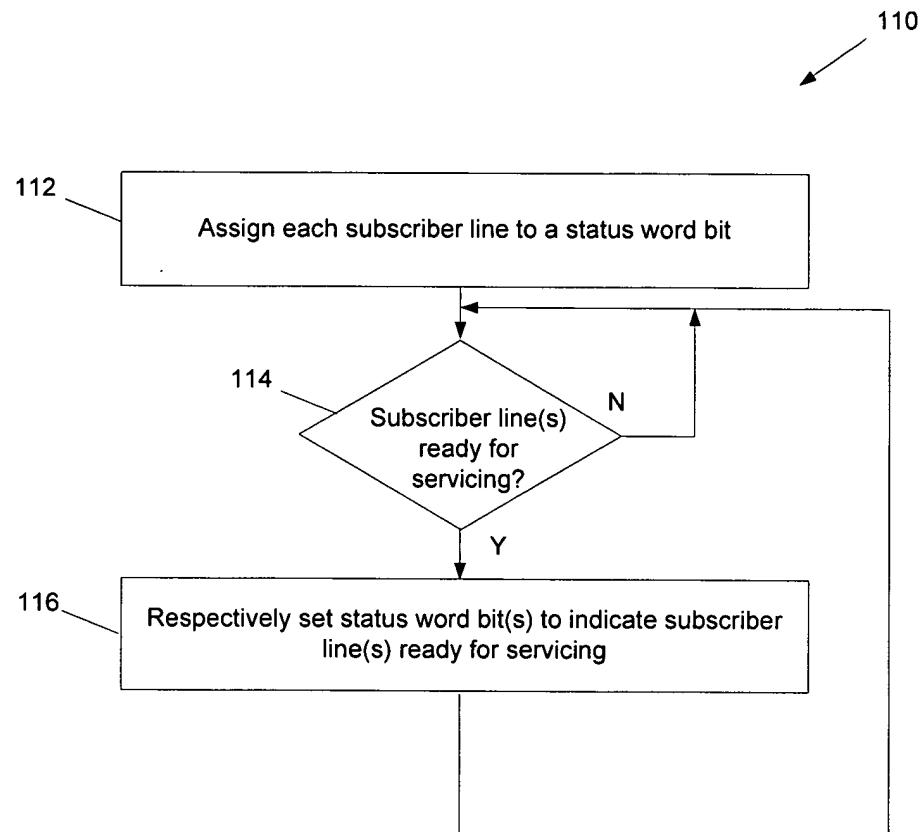


FIG. 5

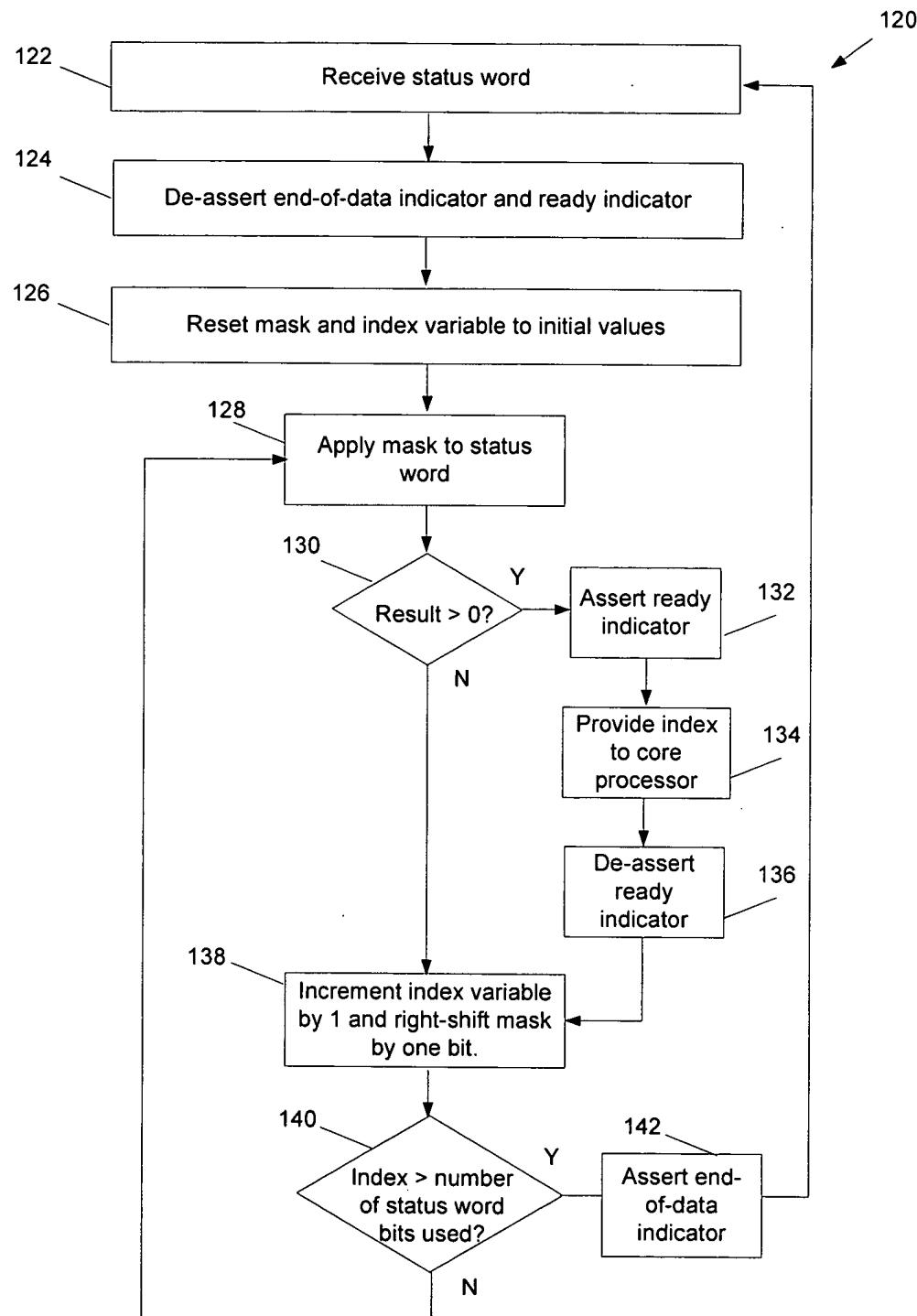


FIG. 6